Greetings hiring team of SiFive:

My name is Qiao, a research assistant from York University Canada I found this job posting on Glassdoor. It arouses my interest as your company specializes in RISC-V architecture, and the job locates in my hometown.

As a bit of self introduction: I specializes in embedded system, I learned my way around sensors (mostly analog) and microcontrollers through various projects I participated in the past years. I have also built PCB boards powering a custom sensor chip that my supervisor developed. I got an A in the course on C and Linux. I have also taken VLSI and analog integrated circuit course and got my hands on Cadence.

My greatest interest lies in building RTOS. I am currently learning it on ARM, but I believe RISC-V is the future, just like Linux. And I would like to contribute my effort in building up RTOS supports for RISC-V. As far as I know SiFive is one of the first if not the first company who creates RISC-V dev-boards. That’s the reason why I would like to work at your company.

So that summarizes my talent and interest. You can contact me by email should my expertise can be of any interest to your company. And I can be available for employment from mid December 2019.

Yours sincerely,

Kuanghua Qiao